

Special Session

Reconfigurable Computing

Organizer and Chair

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This session will provide a forum for researchers and engineers to present their latest research on all aspects of reconfigurable computing, FPGA devices and embedded systems. The topics of interests include (but are not limited to):

1. FPGA-based Computation
2. Novel Reconfigurable Architectures for Signal Processing Applications
3. Low-power Signal Processing Architecture and Implementation
4. Embedded Systems
5. Re-configurable Integrated Circuits
6. High Level Synthesis Tools
7. Design Tools for Signal Processing on Reconfigurable Platforms
8. Novel Algorithms for Signal Processing Reconfigurable Platforms
9. Discrete Applications Processor

Submission:

All papers should be submitted through EDAS system where the interested author should mention the session code (**SP2**) during their submission.

Publications:

All peer reviewed and accepted papers will be published in the conference proceedings.

